

GaAs DOUBLE-BALANCED MIXER

MM1-0312SSM

The MM1-0312SSM is a highly linear passive GaAs double balanced MMIC mixer suitable for both up and down-conversion applications. As with all Marki Microwave mixers, it features excellent conversion loss, isolation and spurious performance across a broad bandwidth and in a small form factor. The MM1-0312SSM is available in a lead-free, RoHS compliant QFN surface mount package and is compatible with standard leaded and lead-free PCB reflow soldering processes. The MM1-0312SSM is a superior alternative to Marki Microwave surface mount M1 and M3 mixers.



Features

- +14 dBm Typical Input 1 dB Compression
- +24 dBm Typical Input IP3
- Compact 3mm QFN SMT Style Package
- Excellent Unit-to-Unit Repeatability
- RoHS Compliant

Electrical Specifications - Specifications guaranteed from -55 to +100°C, measured in a 50Ω system. Specifications are shown for Configurations A & B. See page 2 for port locations.

| Parameter | LO (GHz) | RF (GHz) | IF (GHz) | Min | Typ | Max | LO drive level (dBm) |
|--|----------|----------|----------|-----|------------|-----|--|
| Conversion Loss (dB) (100 MHz) Configuration A Configuration B | 3-12 | 3-12 | DC-4.5 | | 7.5 9 | | |
| Isolation (dB) LO-RF LO-IF RF-IF | | | | | See Plots | | |
| Input 1 dB Compression (dBm) | | | | | +14 +14 | | Config. A: +18 to +23 Config. B: +17 to +23 |
| Input Two-Tone Third Order Intercept Point (dBm) | | | | | +24 +26 | | Config. A: +18 to +23 Config. B: +17 to +23 |

Part Number Options

| Model Number | Description |
|----------------------------|---|
| MM1-0312SSM-2 ¹ | Surface Mount, IF Port Configuration -2 |
| EVAL-MM1-0312S | Connectorized Evaluation Fixture |

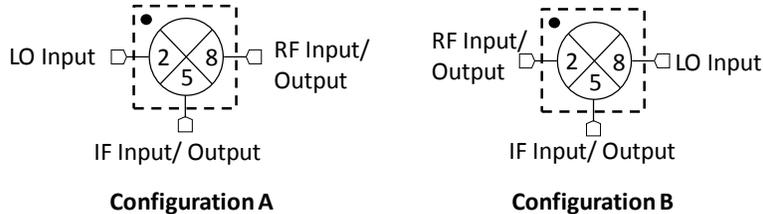
¹ Note: For port locations and I/O designations, refer to the drawings on page 2 of this document.

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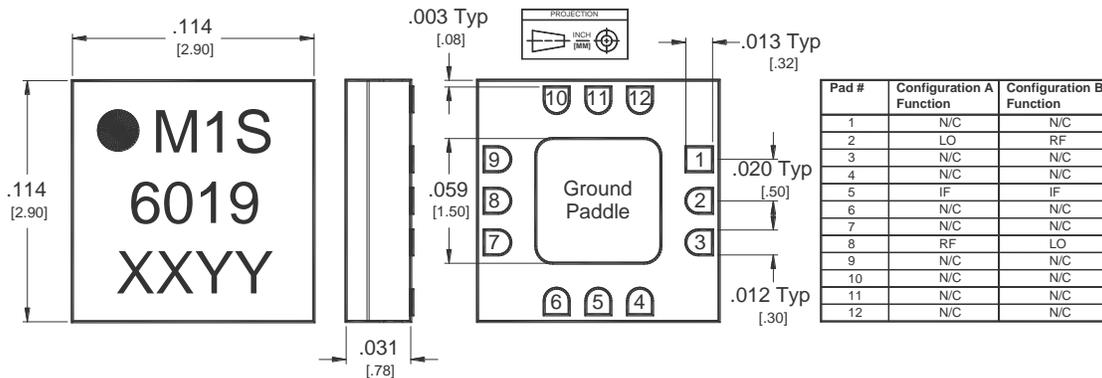
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LO/RF 3 to 12 GHz
IF DC to 4.5 GHz



1. Configuration A/B refer to the same part number (MM1-0312SSM) used in one of two different ways for optimal spurious performance. For the lowest conversion loss, use the mixer in Configuration A (pin 2 as the LO input, pin 8 as the RF input or output). If you need to use a lower LO drive, use the mixer in Configuration B (pin 2 as the RF input or output, pin 8 as the LO input). For optimal spurious suppression, experimentation or simulation is required to choose between Configuration A and B. For more information, [see here](#).



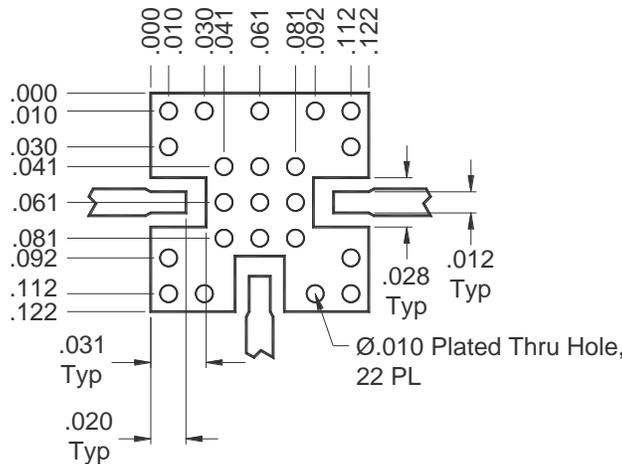
Outline Drawing – 3mm QFN package

Substrate material is Ceramic.

I/O Leads and Ground Paddle plating is (from base to finish):

| | | |
|-----|-------------|------------|
| Ni: | 8.89um MAX | 1.27um MIN |
| Pd: | 0.17um MAX | 0.07um MIN |
| Au: | 0.254um MAX | 0.03um MIN |

All unconnected pads should be connected to PCB RF ground.



QFN-Package Surface-Mount Landing Pattern

[Click here for a DXF of the above layout.](#)
[Click here for leaded solder reflow.](#) [Click here for lead-free solder reflow.](#)

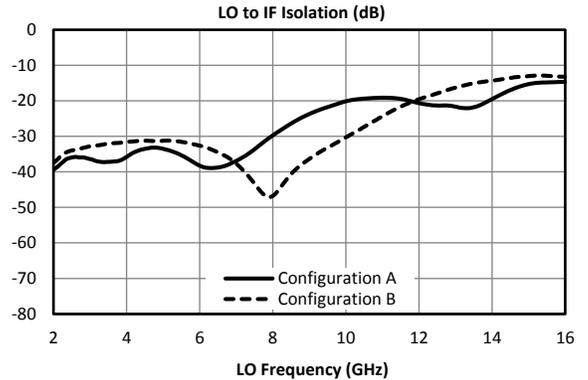
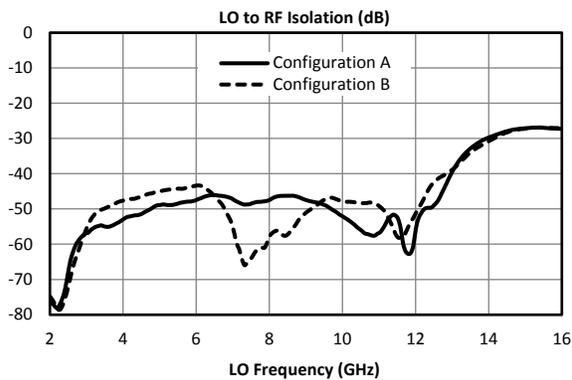
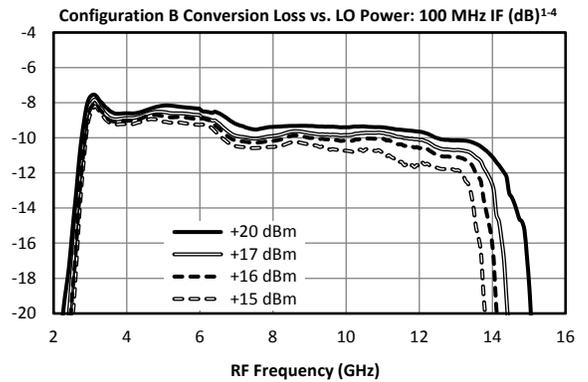
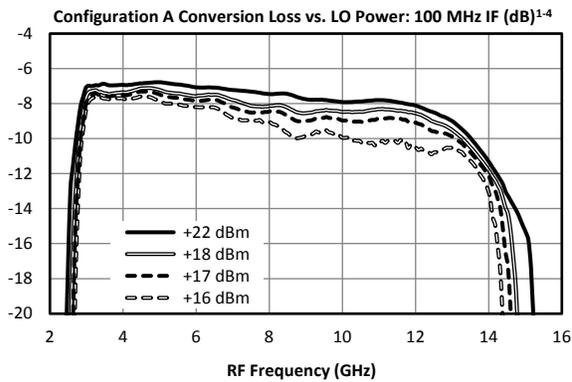
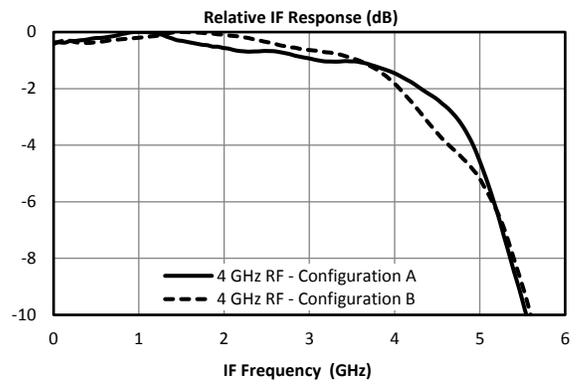
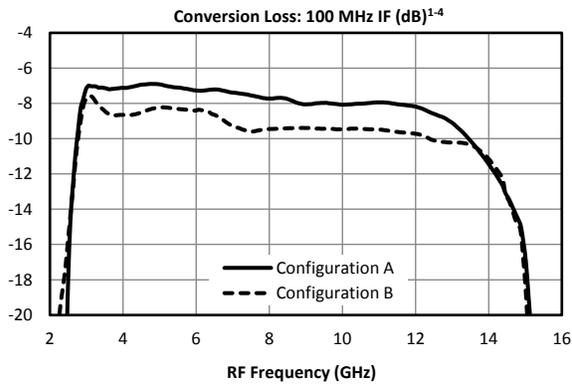
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MM1-0312SSM

LO/RF 3 to 12 GHz
IF DC to 4.5 GHz

Typical Performance



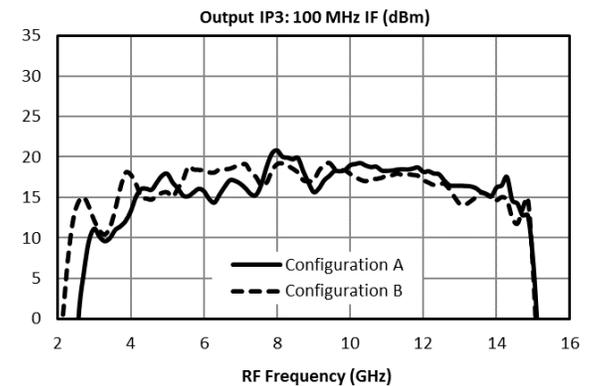
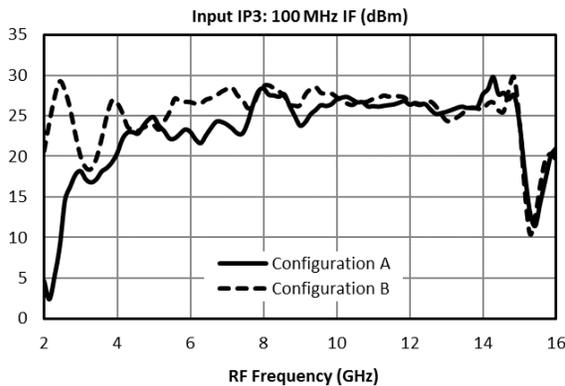
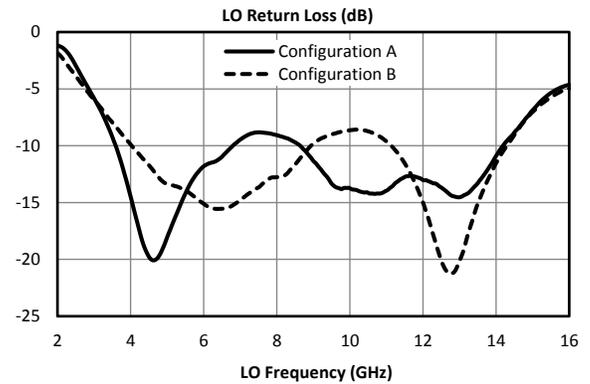
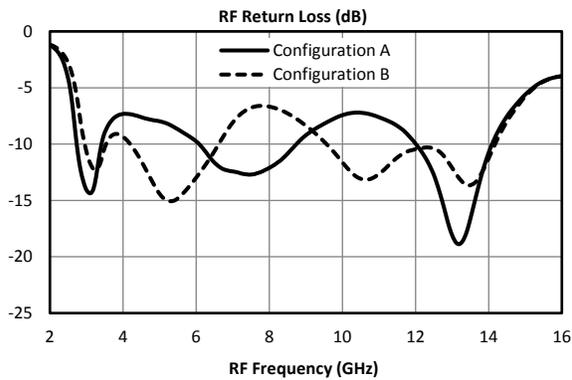
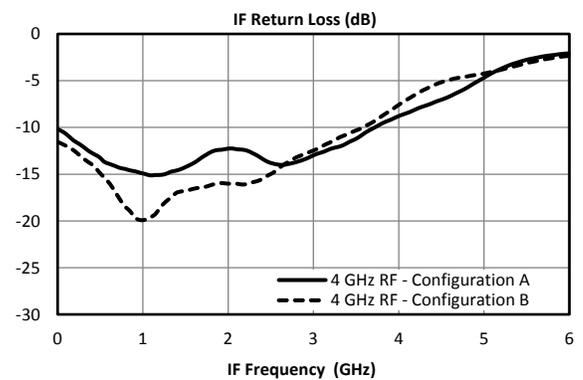
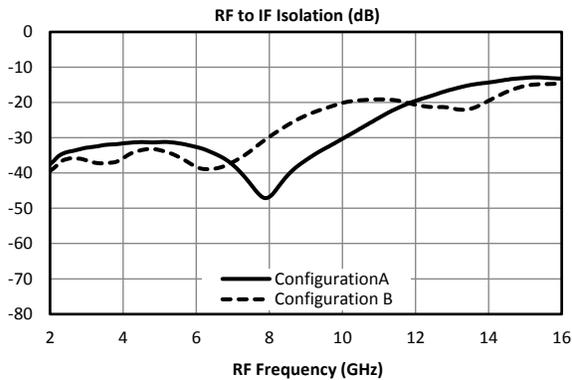
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LO/RF 3 to 12 GHz
IF DC to 4.5 GHz

Typical Performance



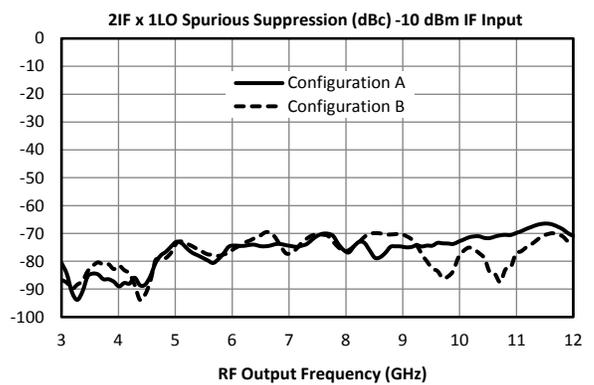
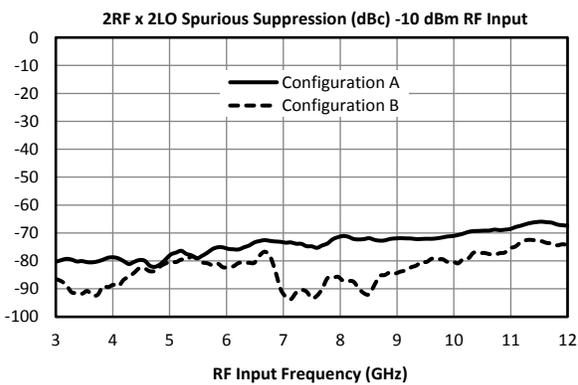
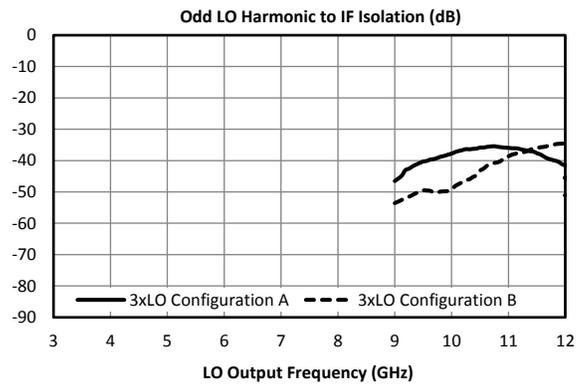
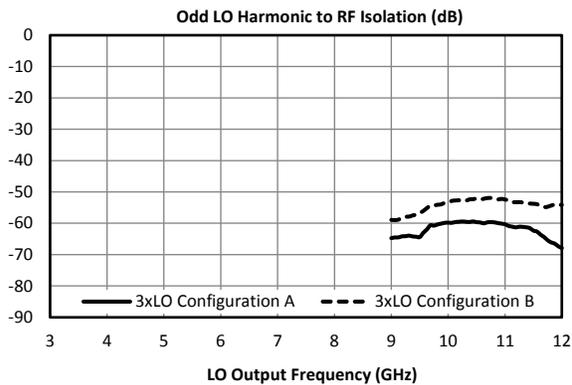
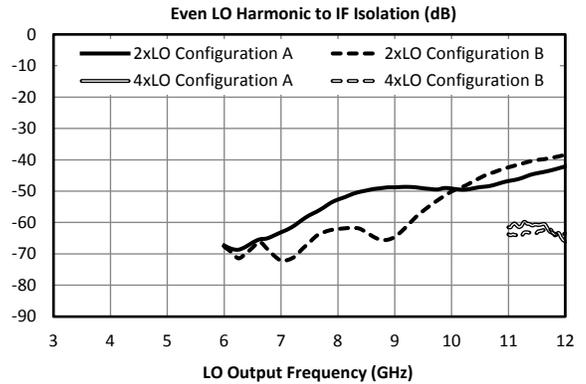
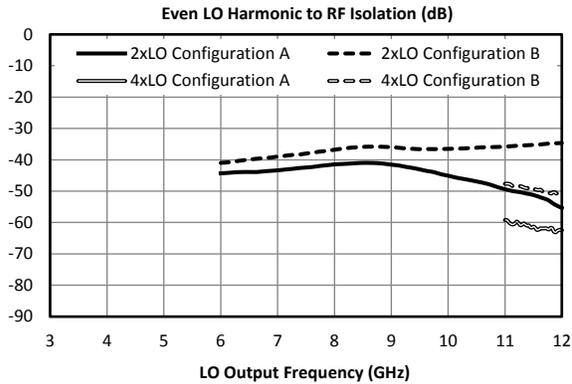
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LO/RF 3 to 12 GHz
IF DC to 4.5 GHz

Typical Performance





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LO/RF 3 to 12 GHz
IF DC to 4.5 GHz

Downconversion Spurious Suppression

Spurious data is taken by selecting RF and LO frequencies ($\pm mLO \pm nRF$) within the RF/LO bands, to create a spurious output within the IF band. The mixer is swept across the full spurious band and the mean is calculated. The numbers shown in the table below are for a -10 dBm RF input. Spurious suppression is scaled for different RF power levels by $(n-1)$, where “n” is the RF spur order. For example, the $2RF \times 2LO$ spur is 74 dBc for the A configuration for a -10 dBm input, so a -20 dBm RF input creates a spur that is $(2-1) \times (-10)$ dB lower, or 84 dBc.

Typical Downconversion Spurious Suppression (dBc): A Configuration (B Configuration)⁴

| -10 dBm RF Input | 0xLO | 1xLO | 2xLO | 3xLO | 4xLO | 5xLO |
|------------------|-----------|-----------|-----------|-----------|-----------|-----------|
| 1xRF | 30 (21) | Reference | 33 (52) | 17 (15) | 37 (49) | 16 (17) |
| 2xRF | 67 (69) | 67 (61) | 74 (83) | 97 (97) | 83 (90) | 78 (72) |
| 3xRF | 112 (112) | 72 (77) | 97 (108) | 84 (88) | 92 (109) | 75 (81) |
| 4xRF | 127 (134) | 127 (122) | 130 (133) | 128 (125) | 136 (137) | 129 (125) |
| 5xRF | 164 (169) | 146 (151) | 148 (156) | 138 (147) | 148 (155) | 139 (148) |

Upconversion Spurious Suppression

Spurious data is taken by mixing an input within the IF band, with LO frequencies ($\pm mLO \pm nIF$), to create a spurious output within the RF output band. The mixer is swept across the full spurious output band and the mean is calculated. The numbers shown in the table below are for a -10 dBm IF input. Spurious suppression is scaled for different IF input power levels by $(n-1)$, where “n” is the IF spur order. For example, the $2IF \times 1LO$ spur is typically 76 dBc for the A configuration for a -10 dBm input, so a -20 dBm IF input creates a spur that is $(2-1) \times (-10)$ dB lower, or 86 dBc.

Typical Upconversion Spurious Suppression (dBc): A Configuration (B Configuration)⁴

| -10 dBm IF Input | 0xLO | 1xLO | 2xLO | 3xLO | 4xLO | 5xLO |
|------------------|-----------|-----------|-----------|-----------|-----------|-----------|
| 1xIF | 27 (28) | Reference | 39 (44) | 14 (13) | 39 (47) | 21 (22) |
| 2xIF | 66 (62) | 76 (77) | 62 (60) | 76 (81) | 74 (68) | 83 (88) |
| 3xIF | 111 (112) | 80 (83) | 94 (105) | 79 (79) | 92 (105) | 81 (82) |
| 4xIF | 126 (114) | 133 (132) | 118 (117) | 140 (137) | 131 (117) | 130 (138) |
| 5xIF | 139 (152) | 135 (138) | 143 (152) | 142 (149) | 152 (156) | 133 (137) |

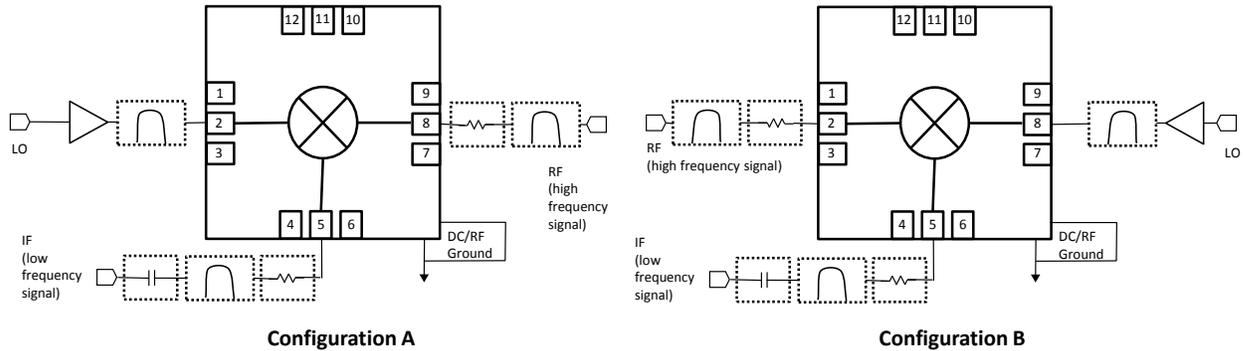
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MM1-0312SSM

LO/RF 3 to 12 GHz
IF DC to 4.5 GHz

Application Circuit



Operation

IF Port – Used as input on an upconversion, output on downconversion, or LO port in a band shifting application. Signals should be connected by 50 ohm microstrip or coplanar traces to well matched broadband 50 ohm sources and loads. Blocking capacitor is recommended if DC voltage is present on the line.

RF Port – Used as input on a downconversion, output on upconversion, or output in a band shifting application. Signals should be connected by 50 ohm microstrip or coplanar traces to well matched broadband 50 ohm sources and loads.

Filtering and Matching- Filtering is generally desired for spurious and image removal on the output port of the mixer. Reflective filters can cause out of band signals to reflect back into the mixer and cause conversion loss ripple, erroneous spurs, and other undesired behaviors. To eliminate these problems it is recommend that the filters be placed as close to the output port as possible. If undesired behavior is still observed, a diplexer with one port terminated or a 1-3 dB attenuator may reduce this problem.

RF Ground – The ground paddle of the QFN should be connected to a low noise RF ground with very low electrical resistance for high frequency operation.

LO Port – The noise floor of the LO input signal should be less than the value of the noise floor plus isolation of the mixer, or a filter is recommended to prevent reduction in dynamic range. An LO amplifier is required if the LO power is below the recommended drive level. It is important to use an amplifier with a broadband 50 ohm match such that it does not reflect spurious signals back into the mixer or other system circuitry.

| Recommended LO Amplifier | | |
|--------------------------|--------------|-----------------|
| Package | Diode Option | Amplifier |
| SM | S | ADM-0126-5835SM |



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LO/RF 3 to 12 GHz
IF DC to 4.5 GHz

| Port | Description | DC Interface Schematic |
|--------|---|------------------------|
| Port 2 | Port 2 is DC open and AC matched to 50 Ohms from 3 to 12 GHz. Blocking capacitor is optional. | |
| Port 5 | Port 5 is DC coupled to the diodes. Blocking capacitor is optional. | |
| Port 8 | Port 8 is DC open and AC matched to 50 Ohms from 3 to 12 GHz. Blocking capacitor is optional. | |

| Absolute Maximum Ratings | |
|---------------------------|---|
| Parameter | Maximum Rating |
| Port 2 DC Current | N/A |
| Port 5 DC Current | 30 mA |
| Port 8 DC Current | N/A |
| RF Power Handling (RF+LO) | +28 dBm at +25°C, derated linearly to +24 dBm at +100°C |
| Operating Temperature | -55°C to +100°C |
| Storage Temperature | -65°C to +125°C |

DATA SHEET NOTES:

- Mixer Conversion Loss Plot IF frequency is 100 MHz.
- Mixer Noise Figure typically measures within 0.5 dB of conversion loss for IF frequencies greater than 5 MHz.
- Conversion Loss typically degrades less than 0.5 dB at +100°C and improves less than 0.5 dB at -55°C.
- Unless otherwise specified, data is taken with +20 dBm LO drive.
- Specifications are subject to change without notice. Contact Marki Microwave for the most recent specifications and data sheets.
- Catalog mixer circuits are continually improved. Configuration control requires custom mixer model numbers and specifications.



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MM1-0312HSM

LO/RF 3 to 12 GHz

IF DC to 4.5 GHz

Revision History

| Revision Code | Revision Date | Comment |
|---------------|---------------|---------------------------|
| - | August 2016 | Initial Release |
| A | April 2019 | Updated plating to ENEPIG |

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215 Vineyard Court, Morgan Hill, CA 95037 | Ph: 408.778.4200 | Fax 408.778.4300 | info@markimicrowave.com
www.markimicrowave.com